

U.S. UTILITY Patent Application

LATENT DATE

APPLICATION NO. 09/917573	CONT/PRIOR 09/917573	CLASS 330	SUBCLASS 296	ART UNIT 2817	EXAMINER Shingetani
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APPLICANTS
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TITLE
Active element bias circuit for RF power transistor input

PTO-2040
12/99

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg	Figs. Drwg	Print Fig 3	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 (Assistant Examiner) 			NOTICE OF ALLOWANCE MAILED	
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